

AMENDMENT UNDER 37 C.F.R. § 1.111

U.S. Appln. No. 09/722,306

Attorney Docket Q61090

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Previously Presented) A ladder circuit editing system for inputting and editing a sequence program for a program controller in the form of a ladder diagram comprising:
 - an unavailable area for storing a plurality of previously stored circuit patterns of circuit elements;
 - an available area for storing circuit elements of a circuit pattern being input;
 - circuit pattern extracting means for making a comparison between a circuit element of the circuit pattern being input and a corresponding circuit element contained in one of the plurality of previously stored circuit patterns stored in said unavailable area, and extracting from the plurality of previously stored circuit patterns an extracted circuit pattern in which the comparison indicates an agreement between the compared circuit elements;
 - display means for displaying the extracted circuit pattern on an input screen; and
 - copying means for copying the extracted circuit pattern into said available area in response to an input by an operator.
2. (Currently Amended) A ladder circuit editing system according to claim 1, wherein:
 - when more than one said extracted circuit pattern is extracted, said display means successively displays, for selection by an operator, the plurality of extracted circuit patterns; and
 - in response to the selection of one of the plurality of extracted circuit patterns, said copying means copies the selected extracted circuit pattern into said available area.

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3. (Original) A ladder circuit editing system according to claim 2, wherein said display means displays a previously selected circuit pattern as a top priority.

4. (Currently Amended) A ladder circuit editing system according to claim 3, further comprising:

a selected circuit pattern address storage area storing ~~the~~ an address of a previously selected circuit pattern; and a

a replacing means for placing the address of the previously a selected circuit pattern at the head of said selected circuit pattern address storage area,

wherein said display means displays as a top priority the previously selected circuit pattern according to ~~the~~ an order of addresses stored in said selected circuit pattern address storage area.